

Figure 1

200

210								> 220				
												-
	ECC	BCC	BCC	ECC	• • •		BCC	CC	ECC			ECC
								Metadata Cache line "D"	Metadata Cache line "D+4"			Metadata Cache line "⊀"
			AND					Metadata Cache line "C"	Metadata Cache line "D+3"			Metadata Cache line "x-1"
	"A" (512 bytes)	"B" (512 bytes)	"C" (512 bytes)	"D" (512 bytes)			"x" (512 bytes)	Metadata Cache line " "B"	Metadata Cache line "D+2"			Metadata Cache line "x-2"
	Data for cache line "A" (512 bytes)	Data for cache line "B" (512 bytes)	Data for cache line "C" (512 bytes)	Data for cache line "D" (512 bytes)	* • *		Data for cache line "x" (512 bytes)	Metadata Cache line "A"	Metadata Cache line "D+1"	* • *		Metadata Cache line "x-3"
	Block Address 0	Block Address 1	Block Address 2	Block Address 3	***	-	Block Address n	Block Address n+1	Block Address n+2	• • •		Block Address n+m

Figure 2

